

Figure /

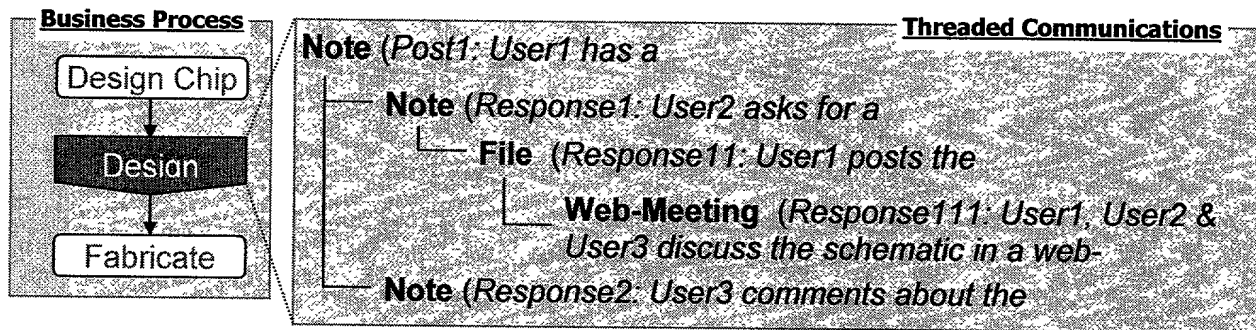


Figure 2

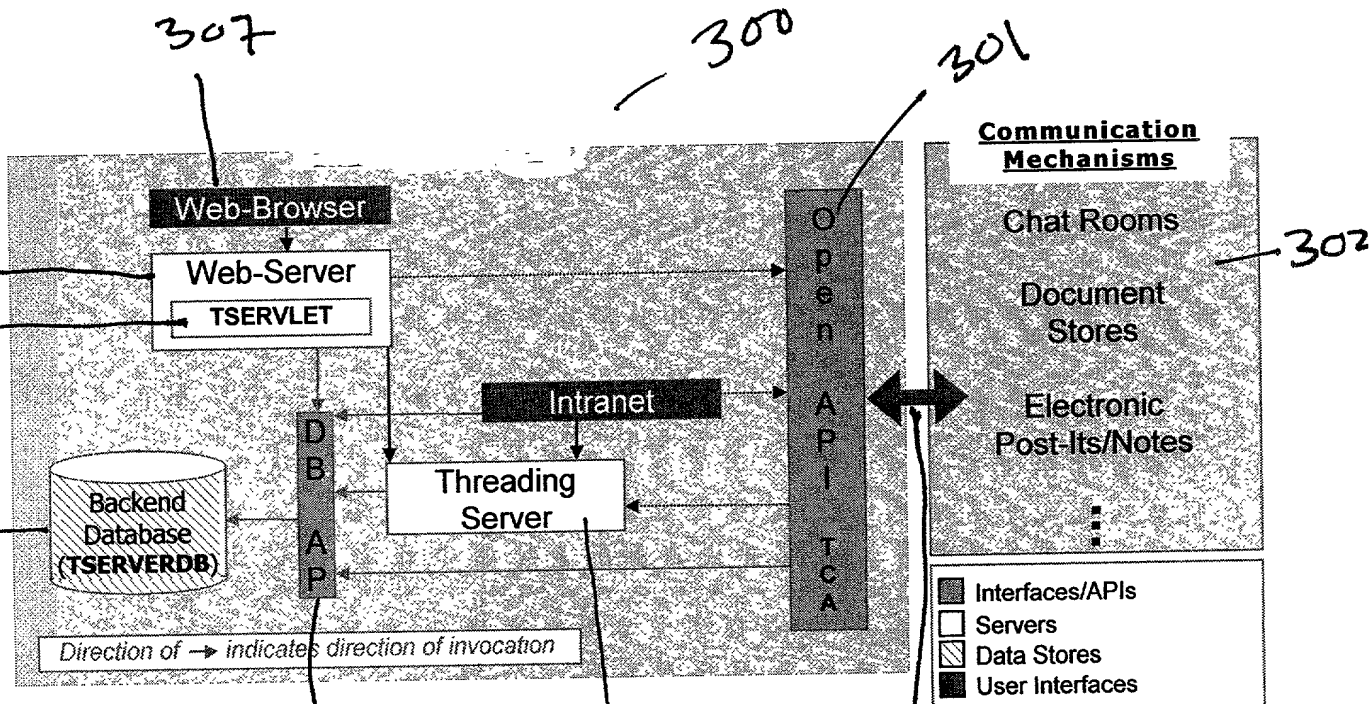


Figure 3:

FIG. 3 is a block diagram of a system architecture.

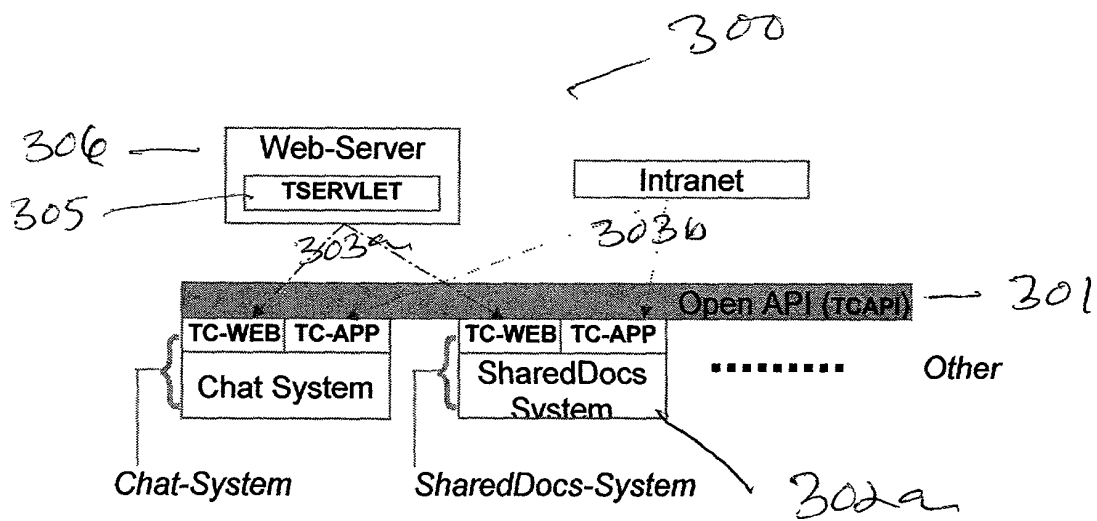


Figure 4:

504

505

Process TopLevel

Detailed Reports • Design Discussions •

Process	Status	Started On	Completed On	
Define Chip	—			Threaded Communications
Design Chip	—			Threaded Communications
Fabricate Chip	—			Threaded Communications

Discussion Thread

506

502

Post New Item •

Title	Owned By	Created On	
Problem with Pin5	John	Dec 10 2001	Respond, Details, Delete
Show me the Schematic	chen	Dec 10 2001	Respond, Details, Delete
Here is the schematic			Threaded Communications for the selected PCONTEXT Respond, Details, Delete
Lets Talk!	ajit	Dec 10 2001	Respond, Details, Delete
Fanout issue?	mona	Dec 10 2001	Respond, Details, Delete

Note

Edit Note •

Title: Fanout issue?

Do you think there is a fanout issue here?

Figure 5

10032110 124901






Show	All			Post New PIE Item	Search	
Title	Owned By	Created On				
What are these errors? What are these 'Unknown pin xxx' errors from lines number 48-72?	vasant	Dec 10 2001		View Thread	Respond	Details Delete
 Synthesis Log File Hi folks - this is the log file for my Synthesis block. Any comments people?	jen	Dec 10 2001		View Thread	Respond	Details Delete
Fanout issue? Do you think there is a fanout issue here?	mona	Dec 10 2001		View Thread	Respond	Details Delete
 Lets Talk! Let us discuss this problem	ajit	Dec 10 2001		View Thread	Respond	Details Delete
 Here is the schematic ALU block Schematic	john	Dec 10 2001		View Thread	Respond	Details Delete
 Show me the Schematic Show me the Schematic	chen	Dec 10 2001		View Thread	Respond	Details Delete
 Problem with Pin5 I think there is some issue with the load on Pin5. The log file seems to indicate something along these lines. Any thoughts folk...	john	Dec 10 2001		View Thread	Respond	Details Delete

Figure 6: